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O Agesen, D Detlefs, A Garthwaite, R Knippel, YS ... - 1999 - portal.acm.org
... The MCS-**lock** uses an atomic **swap** for **lock** acquisition and an atomic **compare**-and-**swap** (CAS) for **lock** release in much the same way as does our meta-**lock ...**
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An Almost Non-Blocking Stack - •hp.com [PDF]
HJ Boehm - portal.acm.org
... at the point at which bl index is **reset** to zero **...** reduce the space overhead for list headers to **near** zero, but **...** attempt to acquire a test-and- set **lock**, we back **...**
Cited by 8 - Related articles - Web Search - BL Direct - All 9 versions

Managing concurrent access for shared memory active messages - •ncu.edu.tw [PDF]
SS Lumetta, DE Culler - Proceedings of the International Parallel Processing ..., 1998 - doi.ieeecs.org
... of high contentionand load imbalance **near** theend oftheexecution **...** The **lock**-free, array-basedqueue algorithmpre- sented in **...** rely on the receiver to **reset** the queue **...**
Cited by 34 - Related articles - Web Search - All 17 versions

[PS] •Architectural issues of creating portable SCI cluster middleware
LP Huse, K Omang, G Krawezik, HO Bugge - Proceedings of SCI Europe 2000 at Euro-Par, 2000 - hjem.ifi.uio.no
... and other high performance interconnects with (**near**) realtime properties **...** is set to zero at processor **reset**, making multi **...** can be achieved by: 1. **LOCK** instruction 2 **...**
Cited by 2 - Related articles - View as HTML - Web Search - All 4 versions

[PDF] •The java. util. concurrent synchronizer framework
D Lea - Science of Computer Programming, 2005 - lambda.csail.mit.edu
... conditional instructions to implement **compare**- AndSetState, that **...** synchronizer classes merely **reset** synchronization state to **...** which a contended **lock** is available **...**
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Cache management system providing improved page latching methodology
P Bumbulis - US Patent 7,383,389, 2008 - freepatentsonline.com
... 6606626, Database system with **lock** manager enhancement for improving **...** of the allocated cache entry is **reset**, the entry **...** will likely be requested in the **near** future **...**
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[DOC] •List of Tables
SF Menke - cs.pitt.edu
... flag) and one of the nodes must continue while the others **reset** their flags **...** for SCRAMNet+ systems and uses it to construct both **lock**-free and **... Compare** and **Swap**. **...**
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[PDF] •Operating Systems and Communications Protocols
L Notes - bedford.gcal.ac.uk
... an I/O instruction they are likely to do so again in the **near** future (eg **...** One way to increase CPU utilisation is to **swap** or evict a blocked process temporarily **...**
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[PDF] •Comparing and Improving Centralized and Distributed Techniques for Coordinating Massively Parallel ...
E Freudenthal - 2003 - cs.nyu.edu
... Saturated rates of combining **near** to memory only occur on sys **...** 7.3 Fetch-and-Increment

Readers-Writers **Lock** . **...** To **compare** the performance of these two families of **...**
Cited by 2 - Related articles - View as HTML - Web Search - Library Search - All 3 versions

[PDF] Scaling Linux® to the Extreme
R Bryant, J Barnes, J Hawkes, J Higdon, J Steiner - Linux Symposium - 202.38.73.198
... Some grid points may be located **near** more rapidly chang- ing **...** was intended to be used only for **reset**- ting the **...** Here, the **lock** was re-acquired to make sure that **...**
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Key authors: **A Silberschatz** - **O Agesen** - **D Detlefs** - **A Garthwaite** - **R Knippel**

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Fault tolerant mutual and k-mutual exclusion algorithms for single-hop mobile ad hoc networks
R Mellier, JF Myoupo - International Journal of Ad Hoc and Ubiquitous Computing, 2006 - Inderscience
... will make mobiles 3 to 10 **reset** their level ... **Fault tolerant** mutual and k-mutual exclusion algorithms for single-hop ... the Step 3 of the generic **MUTEX** algorithm in ...
Cited by 4 - Related articles - Web Search - All 6 versions

A reliable distributed system using dual level **fault** tolerance
JW Hanna, JD Johannes - Southeastcon'92, Proceedings., IEEE, 1992 - ieeexplore.ieee.org
... a bad node will cause the system to be **reset**. ... **mutex** + 1); FIGURE 2 Automatic conversion of waitIsignal ... Dual Level **Fault Tolerant** Summary Table 2 summarizes how ...
Cited by 1 - Related articles - Web Search - All 2 versions

A Modular and **Fault-Tolerant** Data Transport Framework - •arxiv.org [PDF]
TM Steinbeck - Arxiv preprint cs/0404014, 2004 - arxiv.org
... 119 7.5.6 **Fault Tolerant** Event Scatterer . . . . 122 7.5.7 **Fault Tolerant** Event Gatherer . . . .
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[PDF] •A **FAULT TOLERANT** MOBILE IP BASED ON RING PROTOCOL
V VOKKAARNE - 2002 - purl.icla.edu
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[PDF] •Flexible and Reconfigurable Support for **Fault-Tolerant** Object Replication
HP Reiser - vts.uni-ulm.de
... 8 2.2 Architecture of **Fault-Tolerant** CORBA . . . . 94 5.12 Measurements with local computations and **mutex** locks . . . .
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Toggling software characteristics in a **fault tolerant** and combinatorial software environment system, ...
TM Rice, GE Bennett - US Patent 6,634,019, 2003 - freepatentsonline.com
... in the ability to set and **reset** an instruction's ... be useful to include a "**Mutex**" or mutual ... The **fault tolerant** aspect, and the potential encapsulation of all ...
Cited by 2 - Related articles - Web Search - All 3 versions

Fault tolerant and combinatorial software environment system, method and medium
TM Rice, GE Bennett - US Patent App. 11/296,199, 2005 - Google Patents
... 21,2006 (54) **FAULT TOLERANT** AND COMBINATORIAL SOFTWARE ENVIRONMENT SYSTEM, METHOD AND MEDIUM (76) Inventors: Todd M. Rice, Boston, MA (US); Gordon E. Bennett ...
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Validation of a **Fault Tolerant** Microcomputer System 173 6. Conclusions 176 **...**
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[PDF] A Modular and **Fault-Tolerant** Data Transport Framework
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7.5.7 **Fault Tolerant** Event Gatherer . . . . . **...**
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Effective **Fault** Treatment for Improving the Dependability of COTS and Legacy-Based Applications
A Bondavalli, S Chiaradonna, D Cotroneo, L Romano - IEEE TRANSACTIONS ON DEPENDABLE AND SECURE COMPUTING, 2004 - doi.ieeecomputersociety.org
... We forced the application to skip **mutex** unlock functions **...** The resulting **fault**-treatment logic, which is illustrated in **...** of the first -count is also **reset** and an **...**
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Key authors: **C Basile** - **Z Kalbarczyk** - **R Iyer** - **A Benso** - **A Bondavalli**

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[PS] •Architectural issues of creating portable SCI cluster middleware
LP Huse, K Omang, G Krawezik, HO Bugge - Proceedings of SCI Europe 2000 at Euro-Par, 2000 - hjem.ifi.uio.no
... the TICK register is set to zero at processor **reset**, making multi **...** to perform the CAS 2. Desired operation 3. CAS - **compare** & **swap ...** TABLE II **Mutex** performance s]. **...**
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Method for resource lock/unlock capability in multithreaded computer environment
RK Govindaraju, EA Kon, RM Straub, WG Tuel Jr - US Patent 6,112,222, 2000 - Google Patents
... route, disable route, return status, and **reset** adapter **...** process may comprise a hardware **compare** and **swap ...** threads standard, including a pthread **mutex** lock function **...**
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[PDF] •A library implementation of POSIX threads under UNIX
F Mueller - Proceedings of the USENIX Conference, 1993 - moss.csc.ncsu.edu
... Pthreads kernel, the kernel flag is simply **reset** if the **...** 4: Atomic Sequence to Lock a **Mutex** and Record **...** have avoided these prob- lems: Consider a **compare**-and-**swap ...**
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Resource lock/unlock capability in multithreaded computer environment
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... route, disable route, return status, and **reset** adapter **...** process may comprise a hardware **compare** and **swap ...** threads standard, including a pthread **mutex** lock function **...**
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An Almost Non-Blocking Stack - •hp.com [PDF]
HJ Boehm - portal.acm.org
... point at which bl index is **reset** to zero **...** algorithm using a double-pointer-sized **compare**-and- **swap ...** We believe the linuxthreads **mutex** implemen- tation enforces a **...**
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An Efficient Meta-lock for Implementing Ubiquitous Synchronization - •sun.com [PDF]
O Agesen, D Detlefs, A Garthwaite, R Knippel, YS ... - 1999 - portal.acm.org
... type of synchronizable object such as a monitor or a **mutex**. **...** modern architectures provide composite instructions such as **compare**-and-**swap** that read and write **...**
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Language support for lightweight transactions - •utah.edu [PDF]
T Harris, K Fraser - ACM SIGPLAN Notices, 2003 - portal.acm.org
... all accesses to it must be controlled by a given **mutex**,(ii) all **...** memory accesses are atomic and that a word-sized atomic **compare** and **swap** (CAS) instruction **...**
Cited by 300 - Related articles - Web Search - BL Direct - All 22 versions

Managing concurrent access for shared memory active messages - •ncu.edu.tw [PDF]
SS Lumetta, DE Culler - Proceedings of the International Parallel Processing ..., 1998 - doi.ieeecs.org

... As was evident from the microbenchmarks, the Posix **mutex** algorithmis not **...** make use of the double-**compare**-and-**swap ...** but rely on the receiver to **reset** the queue **...**
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Conditional variables without spinlocks
NM Clift - US Patent App. 10/932,475, 2004 - Google Patents
... THE PREFERRED EMBODIMENT [0022] Two synchronization constructs, a **mutex** and a **...** use spinlocks by using any suitable atomic **compare** and **swap** functionality to **...**
Web Search

[PDF] *C++ CSP2: A Many-to-Many Threading Model for Multicore Architectures
AA McEwan, S Schneider, W Ifill, P Welch - twistedsquare.com
... AtomicCompareAndSwap(&(proc->altingState), /***compare**:*/ ALTING_WAITING, /***swap ...** count is first **reset**. **...** another kernel-thread) without having claimed the **mutex**. **...**
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An Almost Non-Blocking Stack - *hp.com [PDF]
HJ Boehm - portal.acm.org
... at the point at which bl index is **reset** to zero **...** We **compare** the following linked stack implementations **...** **Mutex** Each push and pop operation acquires and releases a **...**
Cited by 8 - Related articles - Web Search - BL Direct - All 9 versions

Software for Multiprocessor Networks-on-Chip
M Grammatikakis, M Coppola, F Sensini - Networks on Chip - Springer
... well as synchronization primitives, eg **mutex** lock and **...** a fatal error causes system **reset**, eg submarine **...** For example, **fault** **tolerant** packet routing in the **...**
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[PS] *Synchronization on Cray-T3E Virtual Shared Memory
MD Grammatikakis, S Liesche - ... ://theoretica. informatik. uni-oldenburg. de/mdgramma/**mutex**. ..., 1998 - parsys.informatik.uni-oldenburg.de
... M (a) ; b). Compare&Swap(a; b; c) primitives atomically **compare** the content of memory location **...** my ag = **swap** (Lock, PE **...** short value **reset** = 0, value null = -1 **...**
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SF Menke - cs.pitt.edu
... more than one node has simultaneously written to its flag) and one of the nodes must continue while the others **reset** their flags and **...** **Compare** and **Swap** is a **...**
Related articles - View as HTML - Web Search

[PDF] *Paper Summaries
P Maniatis - berkeley.intel-research.net
... 1 CATEGORIES 5 1.8 **Fault** **Tolerance**, Reliability and Transactions [75]. Rio file cache in [44]. Quicksilver, a transaction-based distributed sys- tem in [?]. **...**
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[PDF] *Synchronization and Concurrency in User-level Software Systems

WN Scherer III - 2006 - cs.rice.edu

... empted — or even takes a page **fault** — while holding a lock prevents any other ... of **compare** and **swap**. Both **compare** and **swap** and load linked/store ...

Cited by 1 - Related articles - View as HTML - Web Search - Library Search - All 4 versions

[PDF] *Design and Verification of Lock-free Parallel Algorithms

H Gao - 2005 - dissertations.ub.rug.nl

... 1.1 Shared memory architectures 5 hardware primitives are test-and-set(TAS), **compare**-and-**swap**(CAS), fetch-and-increment ... **mutex** and several condition variables. ...

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Buddy Threading in Distributed Applications on Simultaneous Multi-Threading Processors - *ncsu.edu [PDF]

N Vouk - 2004 - lib.ncsu.edu

... We **compare** and contrast these architectures and conclude by investigating a ... Currently, many **fault tolerant** computers replicate whole chips, memory controllers ...

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[PDF] *Execution Time Measurements of Processes on the OSE Real-Time Operating System

M Ling - diva-portal.org

... a telecom application with a swapping frequency of 1000 **swap** per second ... process require the same resource under simultaneous execution **mutex**, mutual exclusion ...

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[PDF] *Testing and Verifying Concurrent Objects

JM Wing, C Gong - Journal of Parallel and Distributed Computing, 1993 - cs.cmu.edu

... Moreover, critical regions are ill-suited for asynchronous, **fault-tolerant** systems: if a ... 169 READ returns the integer value pointed to by x. elt **SWAP**(slot s ...

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